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| APPLICATION NO.  | FILING DATE   | ]   | FIRST NAMED INVE   | NTOR  | ATTORNEY DOCKET NO.  | CONFIRMATION NO.   |  |
| 10/750,942   | 01/05/2004 Ho-Cheol   |   | Ho-Cheol Lee   |   | 8956-000107/US   | 2065   |  |
| TITLE OF INVENTION: S<br>SIZE  | EMICONDUCTOR INTEG  | RATED CIRCUIT   | HAVING PADS I  | AYOUT FOR INCRE   | ASING SIGNAL INTEGRIT  | 'Y AND REDUCING CHI  |  |
| APPLN. TYPE  | SMALL ENTITY  | ISSUE FE  | EE P   | UBLICATION FEE  | TOTAL FEE(S) DUE   | DATE DUE   |  |
| nonprovisional   | NO  | \$1400  |  | \$300   | \$1700   | 07/21/2005   |  |
| EXAM   | ART UNIT  |   | LASS-SUBCLASS  | 7   |  |  |  |
| FENTY,   | 2815 25   |   | 257-666000   | _   |  |  |  |
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| Typed or printed name John A. Castellano   |   |   |  | Registration  | No. 35,094   | · · · · ·  |  |
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